10010315\_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returne

From A Search of 10010315 on July 27, 2004

26	714/6	Class	(16 ( 714 :	OR, 10 XR)  ERROR DETECTION/CORRECTION AND FAULT  DETECTION/RECOVERY
		714/100 714/1 714/2 714/3 714/5 714/6		DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING  Reliability and availability  Fault recovery  By masking or reconfiguration  Redundant stored data accessed (e.g., duplicated data, error correction coded data
a, o	r othe	er		parity-type data)
16	711/1	711/100 711/101 711/111 711/112 711/114	(7 O) 711 :	R, 9 XR) ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY STORAGE ACCESSING AND CONTROL .Specific memory compositionAccessing dynamic storage deviceDirect access storage device (DASD)Arrayed (e.g., RAIDs)
6	714/7	Class 714/100 714/1 714/2 714/3 714/5 714/6	(2 OI 714 :	ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY  DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING  .Reliability and availabilityFault recoveryBy masking or reconfigurationOf memory or peripheral subsystemRedundant stored data accessed (e.g., duplicated data, error correction coded da
ta,	or oth	ner		
		714/7		<pre>parity-type data)Reconfiguration (e.g., adding a replacement storage component)</pre>
4	714/5	Class	(2 OF 714 :	R, 2 XR) ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
		714/100		DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

## 10010315\_CLSTITLES

	714/1 714/2 714/3 714/5		<ul><li>.Reliability and availability</li><li>Fault recovery</li><li>By masking or reconfiguration</li><li>Of memory or peripheral subsystem</li></ul>
3	386/111 Class 386/46 386/109	386	RECORDING OR REPRODUCING PROCESSING OF TELEVISION SIGNAL FOR DYNAMIC RECORDING OR REPRODUCING .Compressing in recording or decompressing in reproducing
	386/111		Intraframe or interframe
3	386/126 Class		OR, 3 XR) : TELEVISION SIGNAL PROCESSING FOR DYNAMIC RECORDING OR REPRODUCING
	386/46		PROCESSING OF TELEVISION SIGNAL FOR DYNAMIC RECORDING OR REPRODUCING
	386/125 386/126		.Using disc Optical
3	711/112 Class		OR, 3 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
	711/100 711/101 711/111 711/112		STORAGE ACCESSING AND CONTROL .Specific memory composition .Accessing dynamic storage deviceDirect access storage device (DASD)
3	714/766 Class		OR, 1 XR) : ERROR DETECTION/CORRECTION AND FAULT
			DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING .Digital data error correctionForward correction by block codeMemory accessCheck bits stored in separate area of memory
3	714/770 Class 714/699 714/746 714/752 714/763	(1 714	OR, 2 XR) : ERROR DETECTION/CORRECTION AND FAULT     DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING .Digital data error correctionForward correction by block codeMemory access

```
10010315 CLSTITLES
        714/769
                      ....Dynamic data storage
        714/770
                      ....Disk array
  714/800
                (0 \text{ OR}, 3 \text{ XR})
                714 : ERROR DETECTION/CORRECTION AND FAULT
        Class
                        DETECTION/RECOVERY
        714/699
                      PULSE OR DATA ERROR HANDLING
        714/799
                      .Error/fault detection technique
        714/800
                      .. Parity bit
  714/805
                (0 OR, 3 XR)
        Class
                714 : ERROR DETECTION/CORRECTION AND FAULT
                        DETECTION/RECOVERY
        714/699
                      PULSE OR DATA ERROR HANDLING
        714/799
                      .Error/fault detection technique
        714/800
                      ..Parity bit
        714/805
                      ... Storage accessing (e.g., address parity
                         check)
2 365/200
                 (1 OR, 1 XR)
                365 : STATIC INFORMATION STORAGE AND RETRIEVAL
        Class
        365/189.01 READ/WRITE CIRCUIT
        365/200
                     .Bad bit
2 369/275.3
                 (1 OR, 1 XR)
                369 : DYNAMIC INFORMATION STORAGE OR RETRIEVAL
        Class
                      STORAGE MEDIUM STRUCTURE
        369/272
        369/275.1
                      .Optical track structure (e.g., phase or
                          diffracting structure, etc.)
        369/275.3
                      .. Track data format/layout
2 369/59.25
                (0 OR, 2 XR)
        Class
                369 : DYNAMIC INFORMATION STORAGE OR RETRIEVAL
        369/59.1
                      BINARY PULSE TRAIN INFORMATION SIGNAL
        369/59.25
                      . Format arrangement processing for auxiliary
                         information
2 386/96
                 (1 OR, 1 XR)
        Class
                386:
                       TELEVISION SIGNAL PROCESSING FOR DYNAMIC
                        RECORDING OR REPRODUCING
        386/46
                      PROCESSING OF TELEVISION SIGNAL FOR DYNAMIC
                           RECORDING OR REPRODUCING
        386/95
                      .Having another signal
        386/96
                      .. Audio signal
2
  710/19
                (1 OR, 1 XR)
                710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
       Class
```

PROCESSING SYSTEMS: INPUT/OUTPUT

	710/1 710/15 710/19		10010315_CLSTITLES INPUT/OUTPUT DATA PROCESSING .Peripheral monitoringStatus updating
2 713			OR, 0 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
	711/100 711/101 711/111 711/112 711/113		STORAGE ACCESSING AND CONTROL .Specific memory compositionAccessing dynamic storage deviceDirect access storage device (DASD)Caching
2			OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
	711/100 711/170		STORAGE ACCESSING AND CONTROL .Memory configuring
2			OR, 2 XR) : ERROR DETECTION/CORRECTION AND FAULT
	714/100		DETECTION/RECOVERY DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
	714/1 714/48		.Reliability and availabilityError detection or notification
2	714/755 Class	714	OR, 1 XR) : ERROR DETECTION/CORRECTION AND FAULT     DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING .Digital data error correction
	714/699 714/746		
	714/752 714/755		Forward correction by block codeDouble encoding codes (e.g., product, concatenated)
2 71	714/761 Class		: ERROR DETECTION/CORRECTION AND FAULT
	714/699		DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING
	714/746 714/752 714/761		.Digital data error correctionForward correction by block codeRandom and burst error correction
2 7	714/763 Class		OR, 1 XR) : ERROR DETECTION/CORRECTION AND FAULT
	714/699		DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING

	714/746 714/752 714/763	10010315_CLSTITLES .Digital data error correctionForward correction by block codeMemory access
2		1 OR, 1 XR) 4: ERROR DETECTION/CORRECTION AND FAULT     DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING     .Digital data error correction    Forward correction by block code    Memory access    Code word for plural n-bit (n>1) storage units (e.g., x4 DRAM's)
2	714/785 (Class 71 714/699 714/746 714/752 714/781 714/785	0 OR, 2 XR) 4: ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING Digital data error correction Forward correction by block code Code based on generator polynomial Syndrome computed

PLUS Search Results for S/N 10010315, Searched July 27, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated sear ch

system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that a re

most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

Page 1

## 10010315\_LIST

5222086
5237658
5263145
5274799
5390187
5398253
5469453
5490248
5499337
5519844
5526482
5600816
5613059
5617530
5708769